

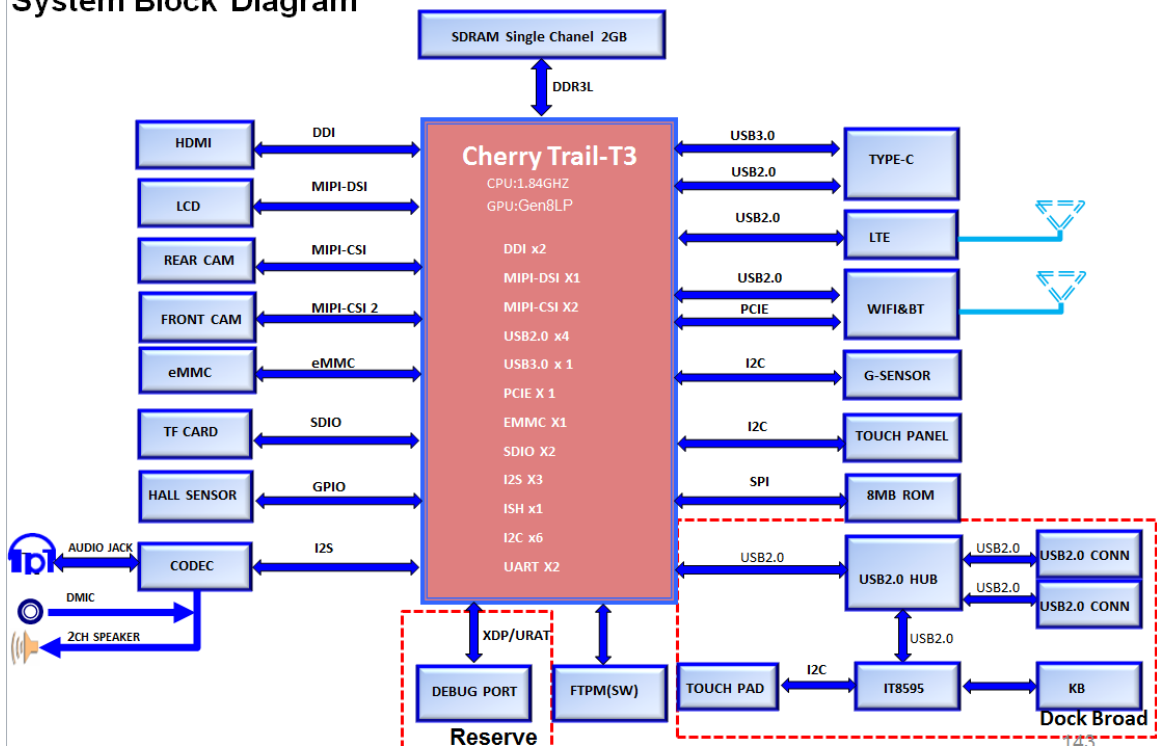
BM5668 V1.5A_R LPDDR3 Realtek

Intel Cherry Trail T3 Platform

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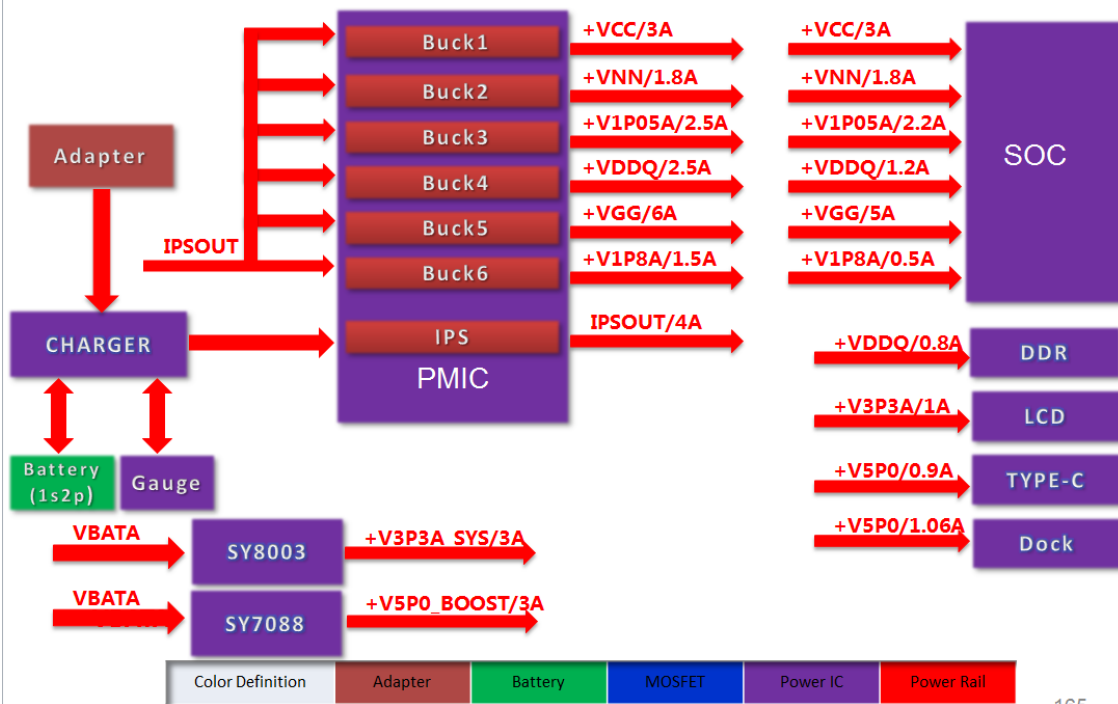
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System Block Diagram



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Power Block Diagram



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History

- P5 Add M0_MA15 for Micron 4GB memory down
- P14 Delete level shift circuit
- P14 Change charger I2S power rail for S5 charger issue
- P16 Change camera 1.2V from +V1P8_ELD03 to +V1P2SX
- P16 Change Sensors power rail to +V3P3_ALDO3 follow MRD
- P18 Change 5V boost IC from SY7088 to ETA1096 for current limit issue
- P19 OPEN LCD 3.3V power control for cost down
- P19 PR78 change to 2.7 ohm , PR75 changr to 0 ohm
- P28 reserve I2C port 2 for Gsensor
- P32 change 2M camera I2C to port 0 follow MRD
- P34 Delete Touch Panel level shift circuit
- P38 Redriver IC D1P/M need connect to D2P/M

DVT to SIT changelist

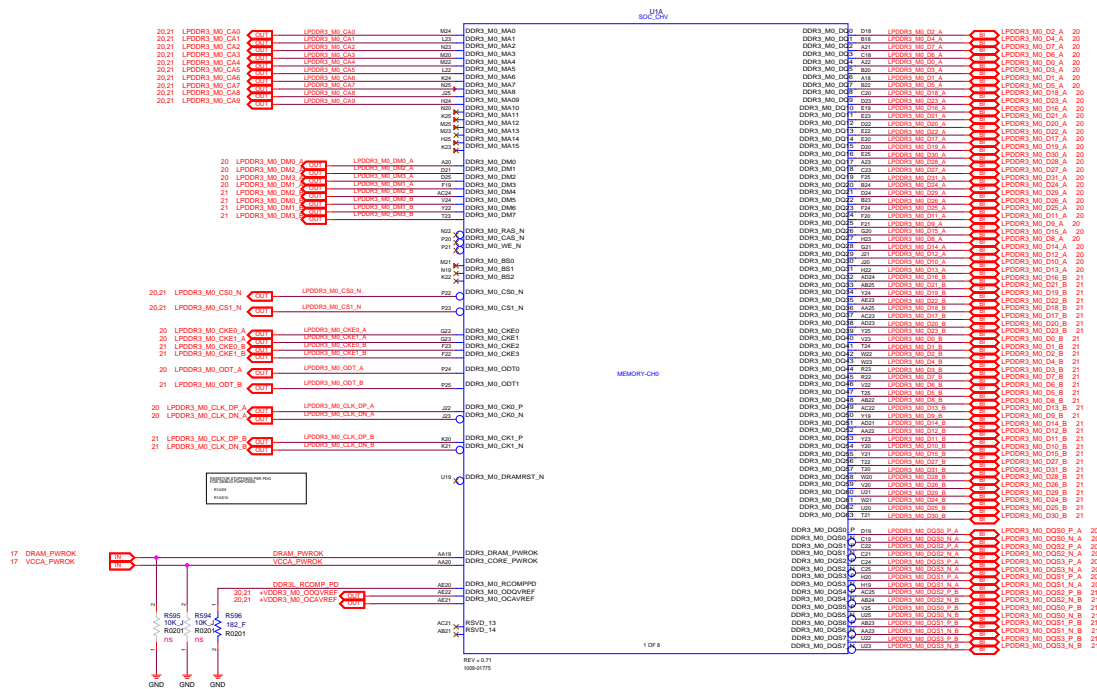
- P13 Add C631,C630 for EMI
- P24&P30&P31 colay 0 ohm footprint change to 0402
- P15 PMIC pin66 change to NC(reserve TP) for LPDDR3 VDDQ
- P17 PD2 change to LBAT54XV2T1G used on Force
- P18 Add PC201,change PR63 and PR62 for USB Drop
- P19 Change PR78 to 3.9 ohm for PU8 revsion update
- P23 Add R611 for debug
- P20&P21 DDR3L change to LPDDR3
- P29 Add level shift for LITEON wifi
- P33 R232 just reserve;
LED control change to +V1P2A from S0ix;
Switch and SAR Power change to +V3P3A_PLT from +V3P5A_SYS
- P34 CAP change to 47pf from 0.1uf for touch panel has no function
- P36 R214 change to ns for nokia dection
- P37 USB signal change to pin6,pin7 because pin1 connect to power
- P38 Q6 change to transistor,add R566;
Yellow LED change to WHITE_LED control
Reserve C629 for EMI
R303,R304 change to 680 for LED bright

SIT to SVT changelist

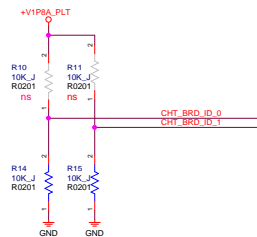
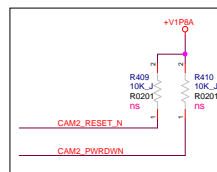
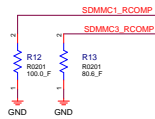
- P09 change C3 C4 to 8.2pf for RTC
- P28 change R200 position for ST sensor
- P34 change TP connector

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SOC:MEMORY



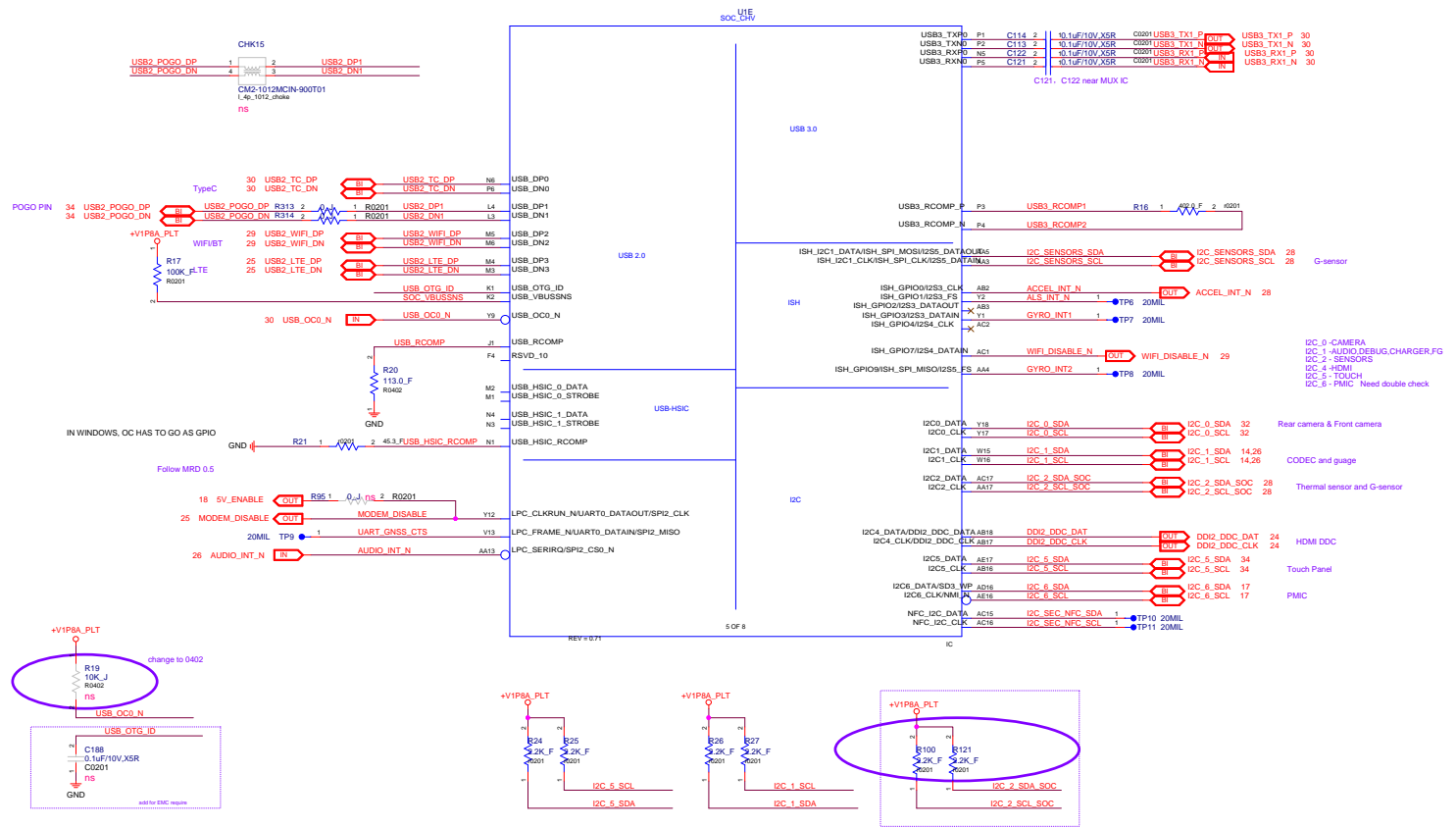
+V1P8A_PLT 6,8,9,14,16,17,18,34
 +V1P8A 12,15,16,20,22,23,24,25,26,27,28,29,31,33

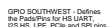
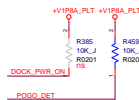
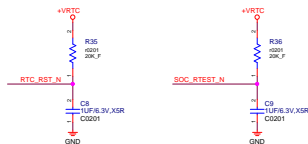
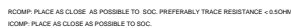
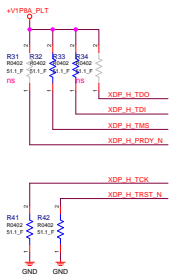


	ID0	ID1
2GB DDR3L	0	1
4GB LPDDR3	0	0

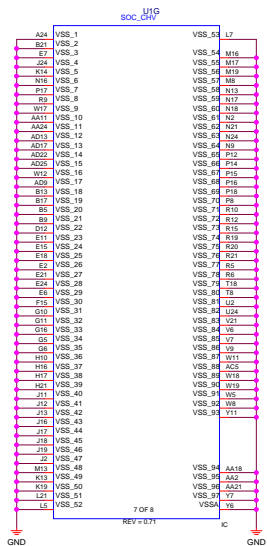
SOC : USB/HIS/I2C/ISH

+V1P8A_PLT 6,7,9,14,16,17,18,34



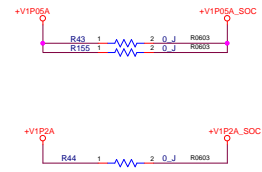
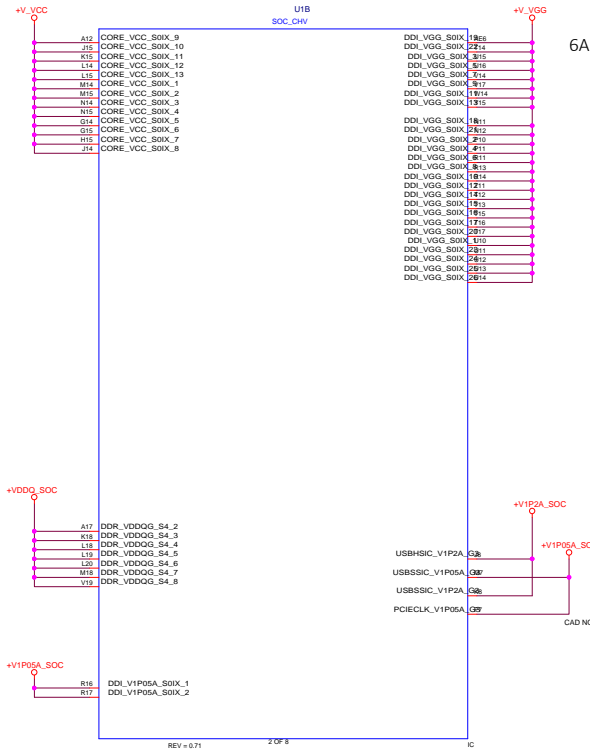


SOC : GND



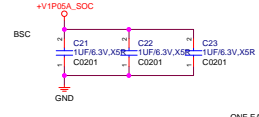
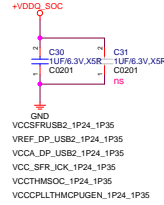
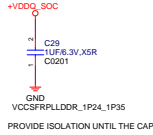
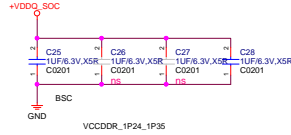
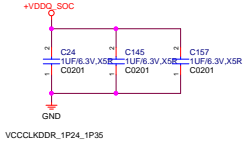
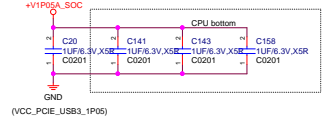
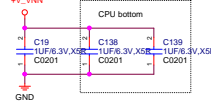
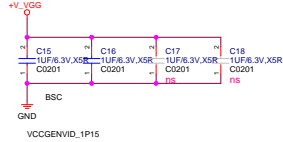
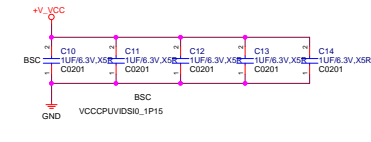
- +V1P05A_SOC 12,13
- +V1P05A 15,18
- +V_VGG 13,15
- +V_VCC 13,15
- +VDDG_SOC 12,13,15
- +V1P2A 16,33
- +V1P2A_SOC 12,13

SOC POWER 1
3A



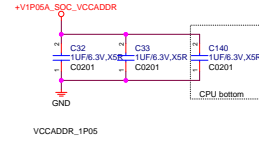
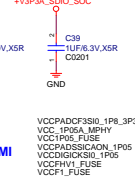
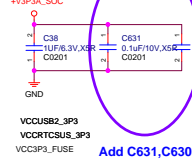
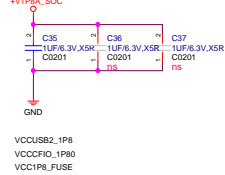
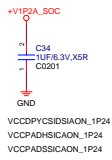
+V_VGG 11,15
 +V_VNN 12,15
 +V_VCC 11,15
 +VDDQ_S0C 11,12,15
 +V3P3A_S0D0_S0C 12,16
 +V1P2A_S0C 11,12
 +V3P3A_S0C 12
 +V1P05A_S0C 11,12
 +V1P05A_S0C 12

SOC:DECOUPLING



VCCSRAMGEN_1P15
 VCCFHVCPUSIO_1P15
 VCCFHVCPUSIO_1P15
 VCCSRAMSOC1_1P05
 VCCSRAMSOC2_1P05
 VCCSRAMSOC_1P15
 VCCRAMCPUS1_1P15
 VCC_1P05A_MPHY
 VCC1P05_FUSE
 VCCFHV1_FUSE
 VCCF1_FUSE
 VCCPADSSICAON_1P05
 VCCDIGICKSIO_1P05

ONE EACH AT EAST AND WEST QUADRANT

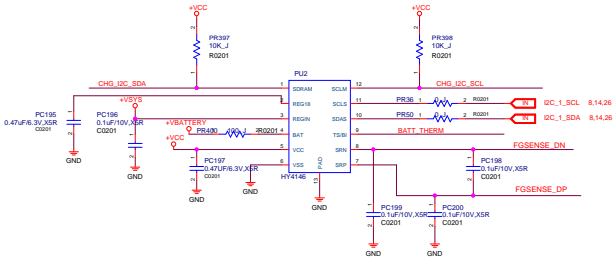
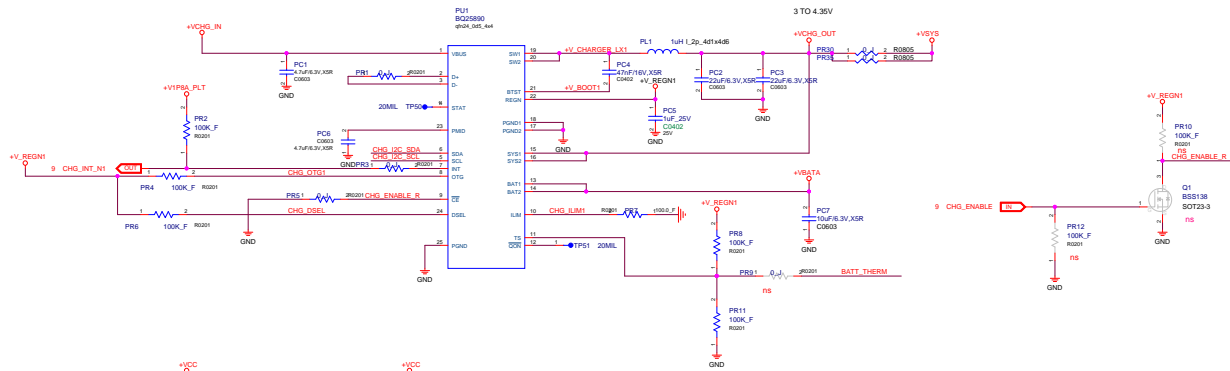


Add C631, C630 for EMI

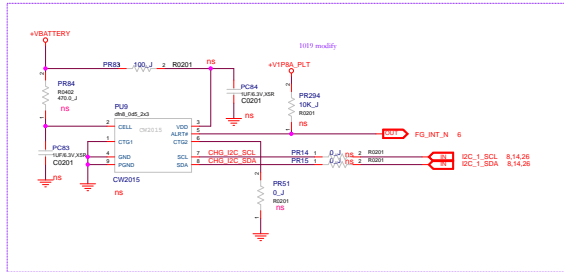
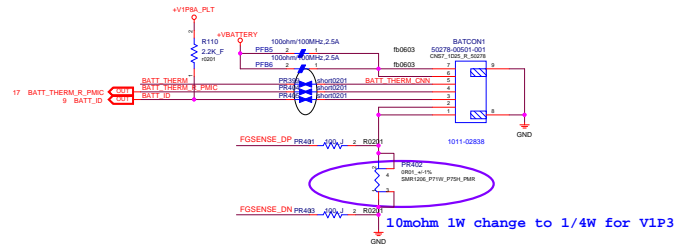
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+VCHG_IN 15,30
 +VDS 15,16,19
 +VBATTERY 15
 +VIPBA_PLT 8,7,8,16,17,18,34
 +VBA 15

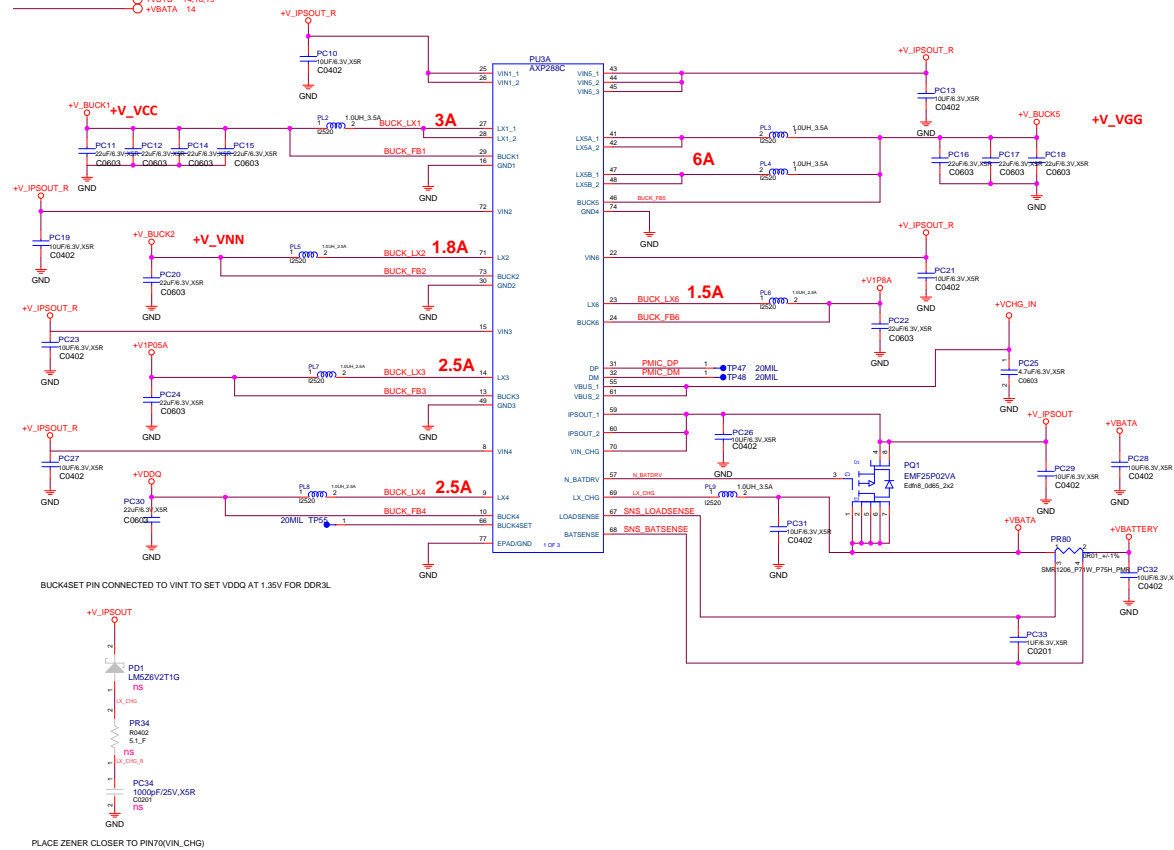


BAT CONN



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V+ VPS05A_R 16,18
 V+V105A 11,18
 V+ VGG 11,13
 V+V18A 7,12,16,20,22,24,25,26,27,28,29,31,33
 V+VCHG_IN 14,30
 V+BATTERY 14
 V+VPS0A_SYS 18,19,25
 V+ VNN 12,13
 V+ VGC 11,13
 V+VSYS_PMIC 16
 V+VDDO_DOR 20,21
 V+VDDO_SOC 11,12,13
 V+VSYS 14,18,19
 V+BATA 14
 V+ VPS05A_R



Default:
PMIC: BUCK-VSYS
EXTCHG:EXTCHG-VSYS

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- +V_IPSOUT_R

15,18

+V1P8A

7,12,15,20,22,23,24,25,26,27,28,29,31,33

+V3SYS_PMIC

15

+V2P8_ALDO1

32

+V1P8A

9,12

+V1P8A

11,33

+V2P8A_DLD01

23
- +V3P3A_WIFI

18,29

+V1P8A_PLT

6,7,8,9,14,17,18,34

+V3P3SX_TOUCH

34

+V3P3A_DLD03

19

+V3P3A_SDIO_SOC

12,13

+V3P3A_PLT

12,17,28,33

+V1P2SX

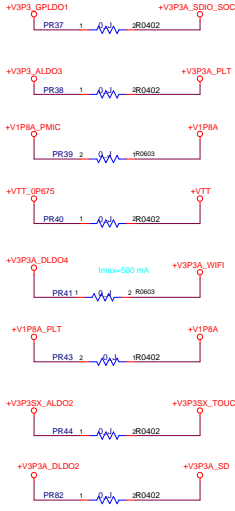
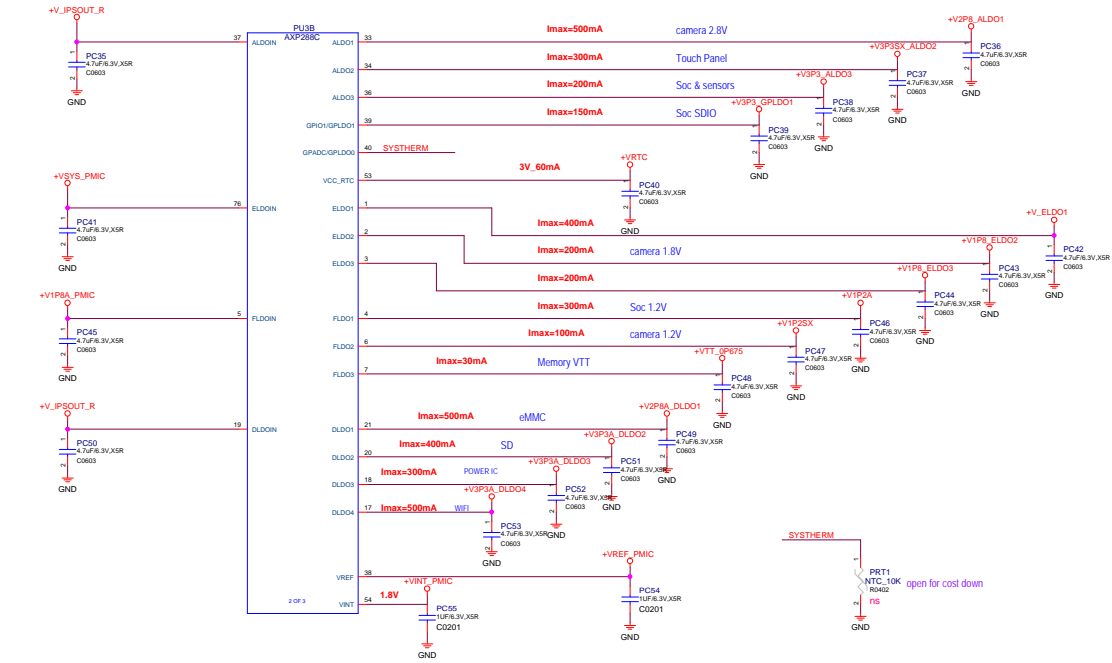
32

+V1P2SX

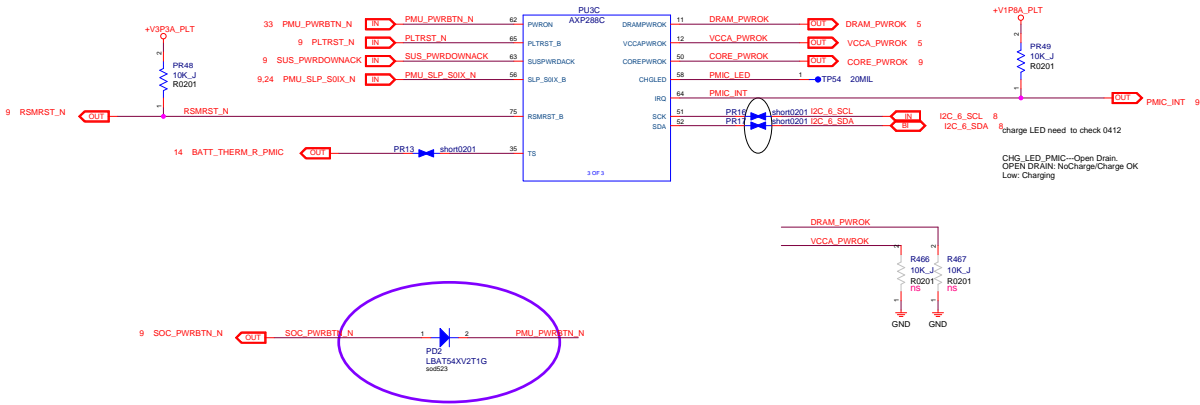
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+V3P3A_SD

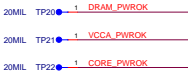
18



+V3P3A_PLT 12,16,28,33
+V1P8A_PLT 6,7,8,9,14,16,18,34
+VCHG_IN 14,15,30

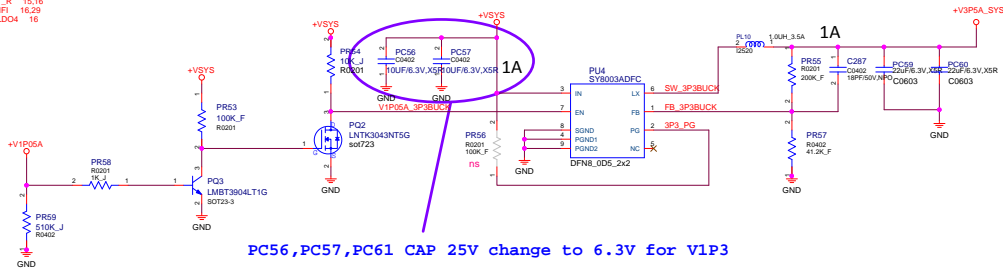


chage diode for restart because of leakage current too large



- +V1P05A 11,15
- +V3P5A_SYS 15,19,25
- +V1P8A_PLT 6,7,8,9,14,16,17,34
- +V_SP0_BOOST 24,26,30,34
- +VSYS 14,15,19
- +V_IPSOUT_R 15,16
- +V3P3A_WIFI 16,29
- +V3P3A_DLD04 16

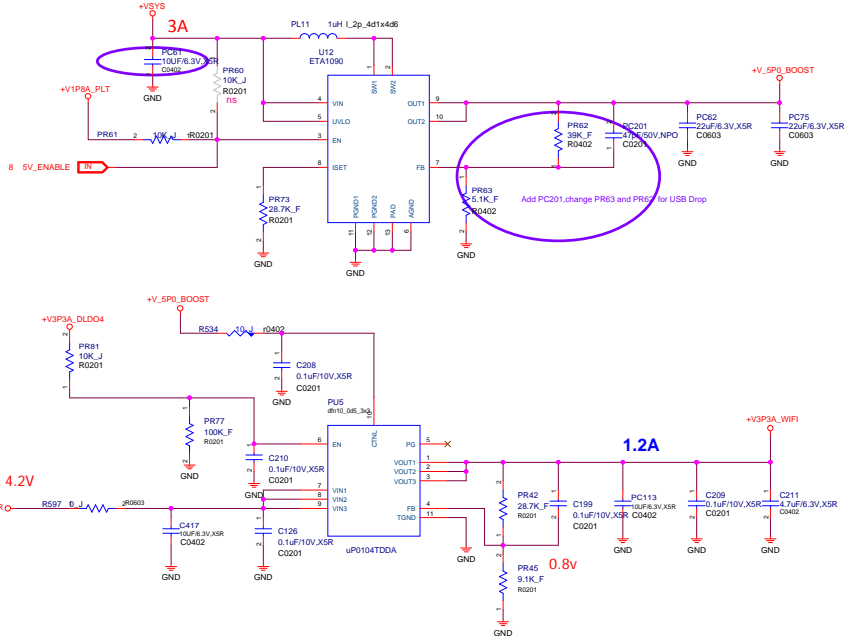
3.3V BUCK REGULATOR



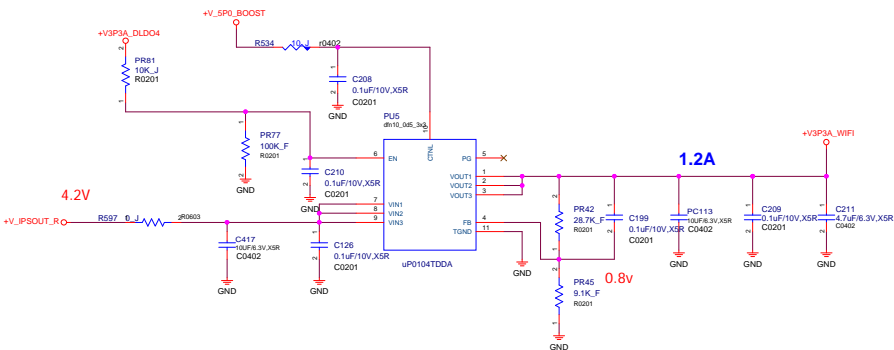
PC56,PC57,PC61 CAP 25V change to 6.3V for V1P3

SENSE 07/24

增加三极管和电阻，用电流开启，之前用mos管会出现因为开启电压太低开不起的情况

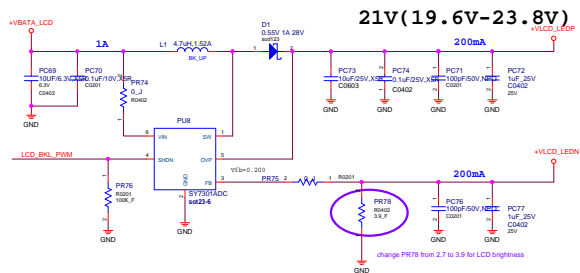
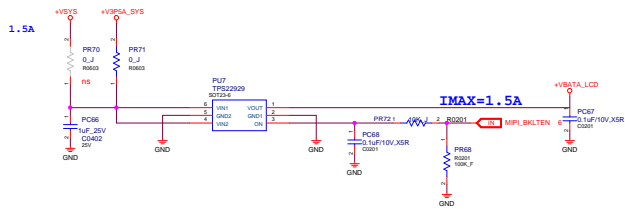
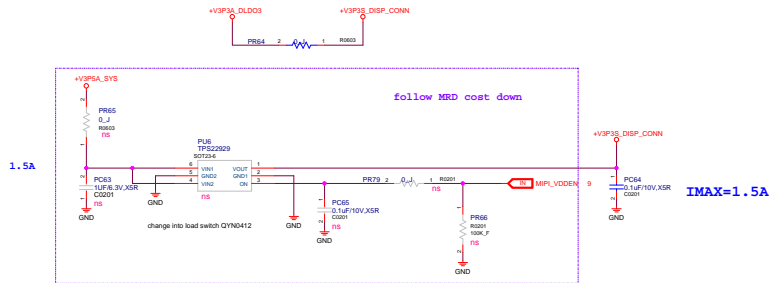


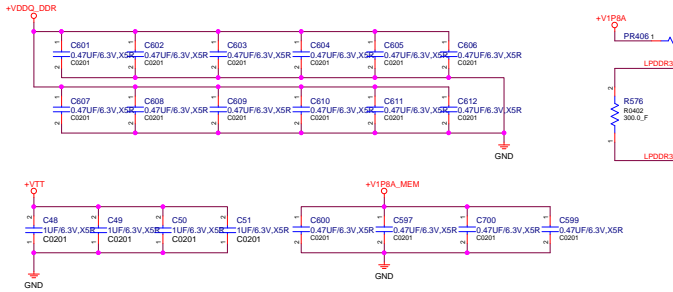
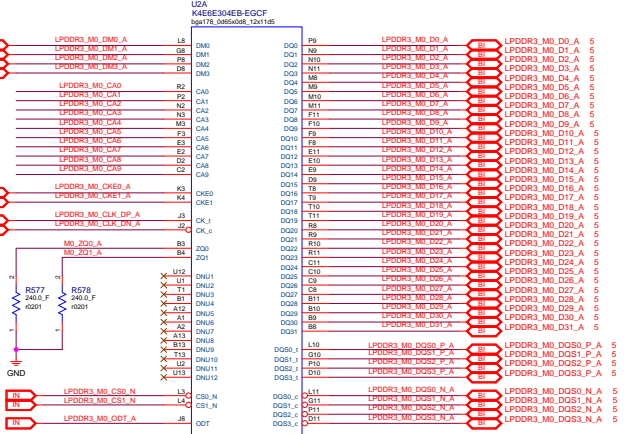
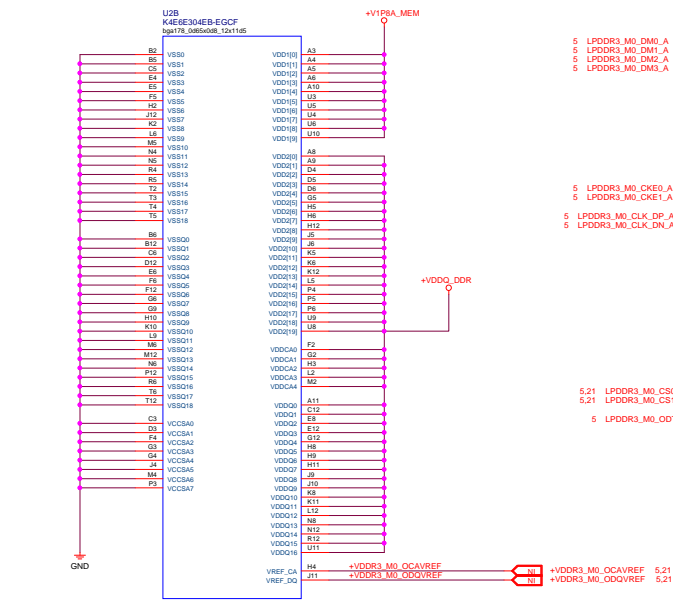
Add PC201, change PR63 and PR65 for USB Drop



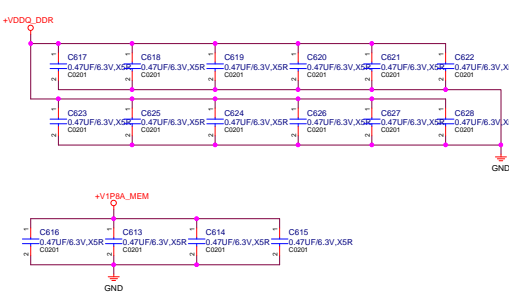
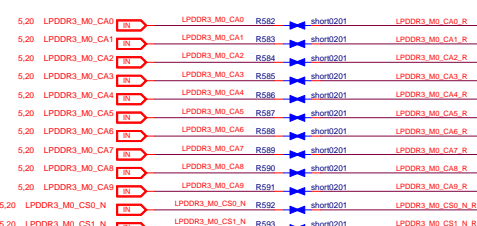
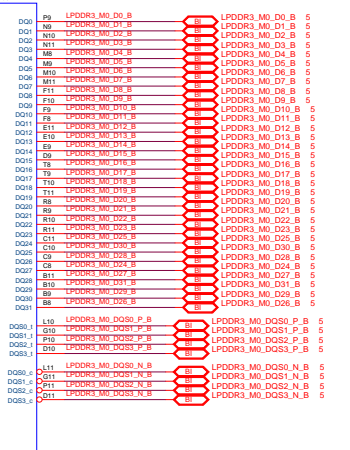
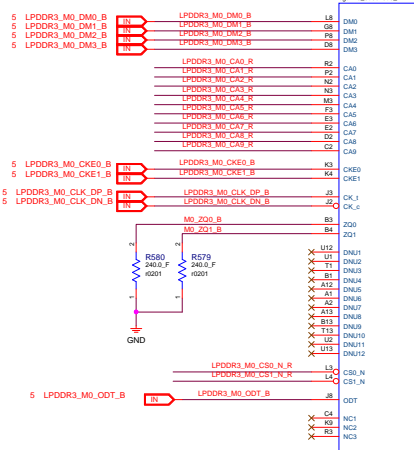
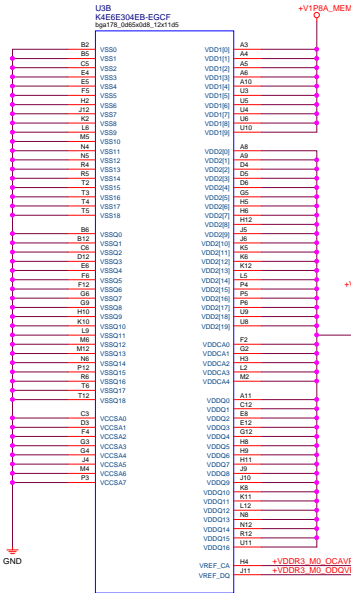
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- +VSP3A_DLD03 16
- +V_SPL_B0001 18,24,26,30,34
- +VSP3A_SYS 15,18,20
- +VSP3A_DISP_CONN 31
- +VLCD_LED0P 31
- +VLCD_LED0N 31
- +VSP3S 14,15,18

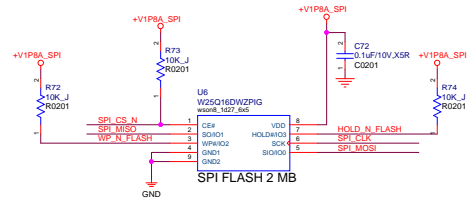
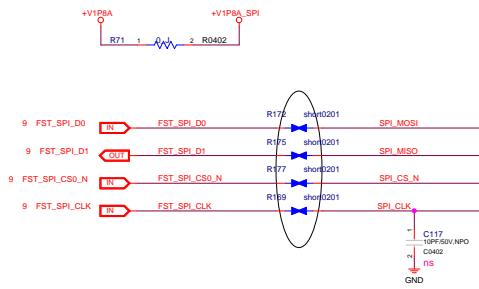




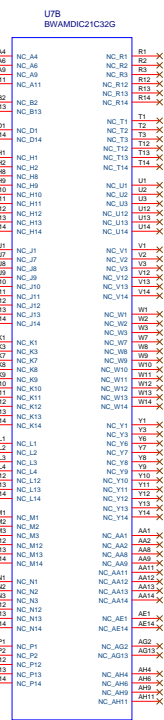
- +V1P8A 7,12,15,16,20,22,23,24,25,26,27,28,29,31,33
- +V1P8A_MEM 20
- +VDDQ_DDR 15,20

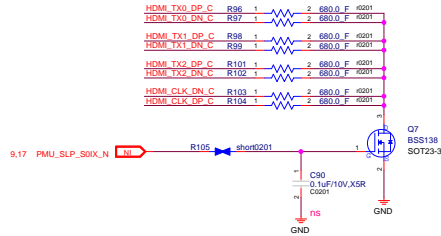
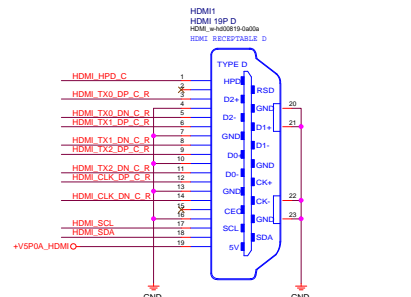
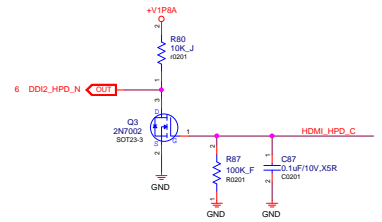


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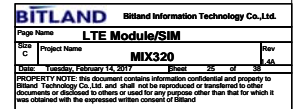
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Custm		Rev	
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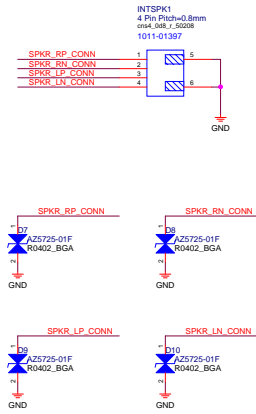
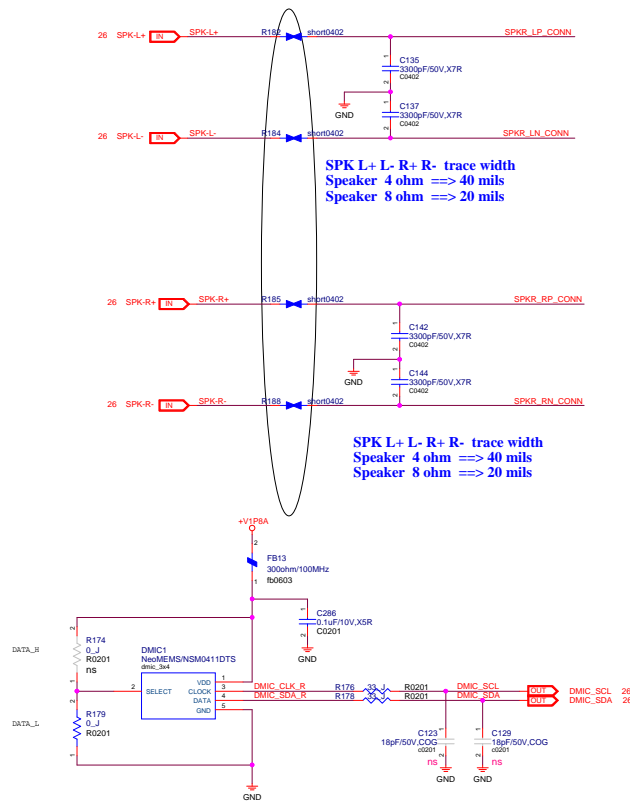


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Speaker 1W 8ohm



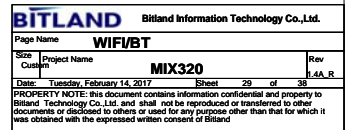
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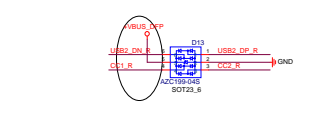
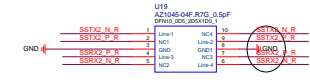
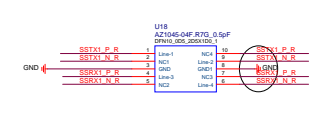
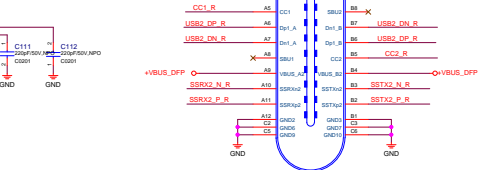
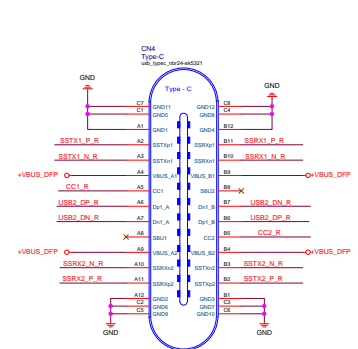
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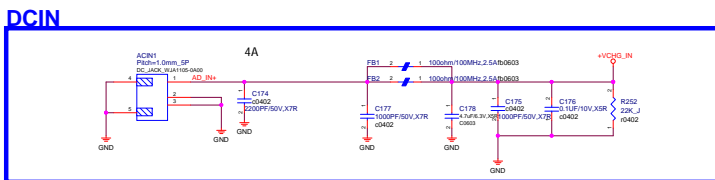
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- 0201 change to 0402 footprint for DVT bug

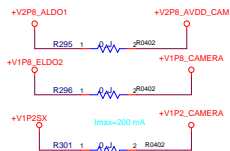
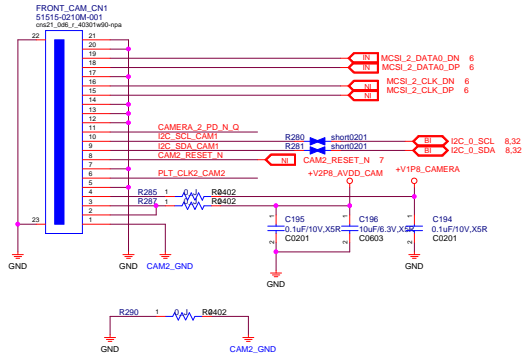


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<p>PRECEDENCE NOTICE: this document contains information confidential and property to Bitland Technology Co., Ltd. and shall not be reproduced or transferred to other documents or disclosed to others or used for any purpose other than that for which it was obtained with the expressed written consent of Bitland</p>			

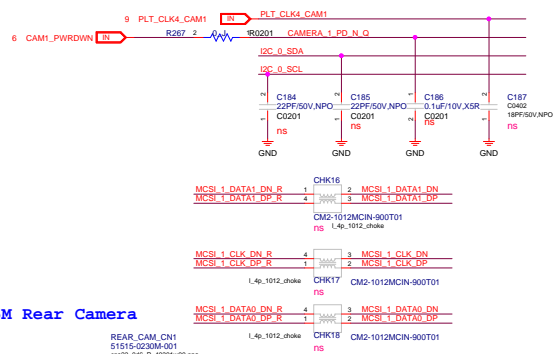
Front camera connector

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delete series resistor
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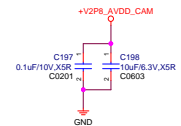
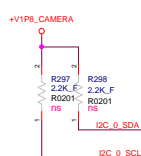
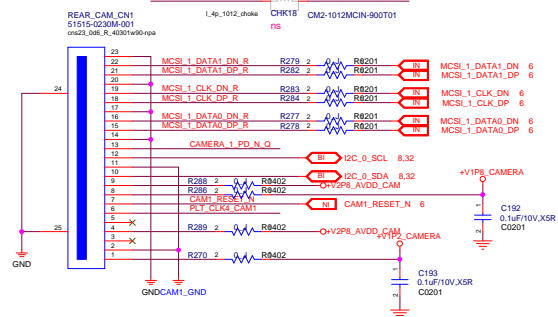
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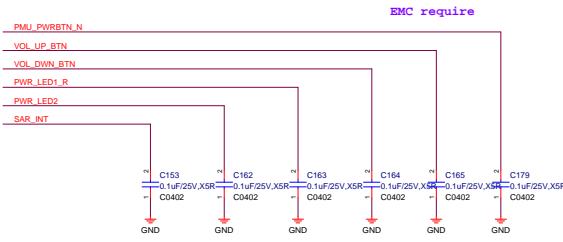
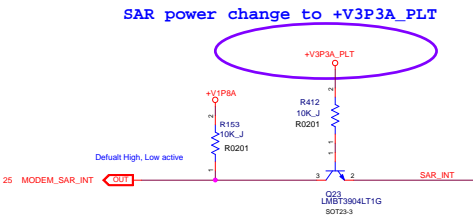
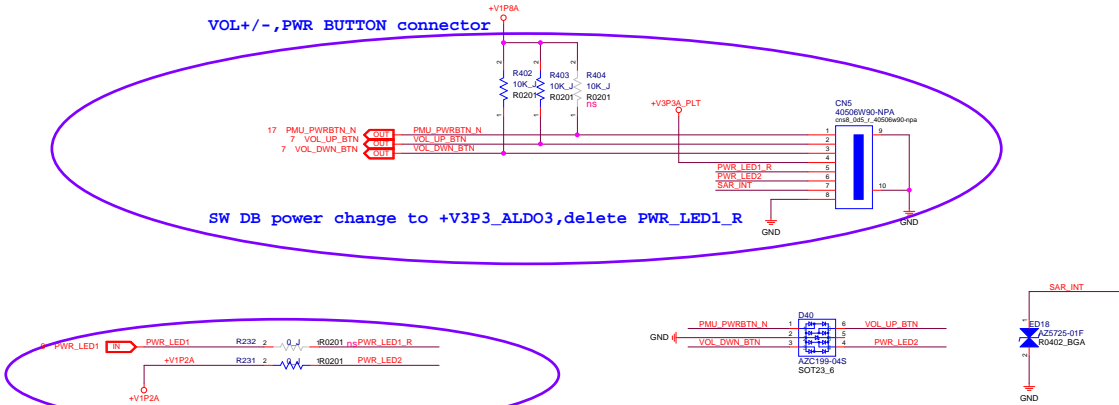
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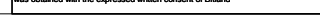


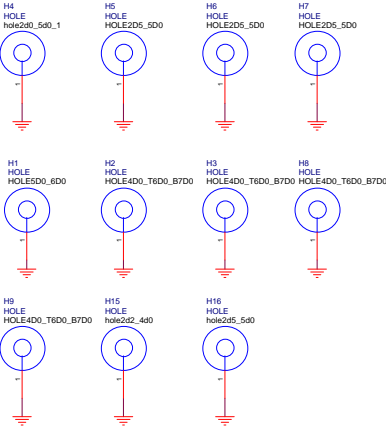
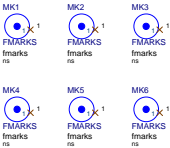
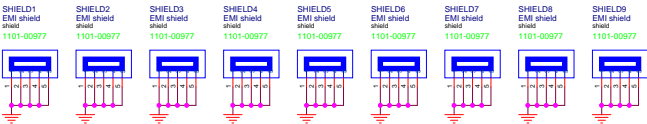
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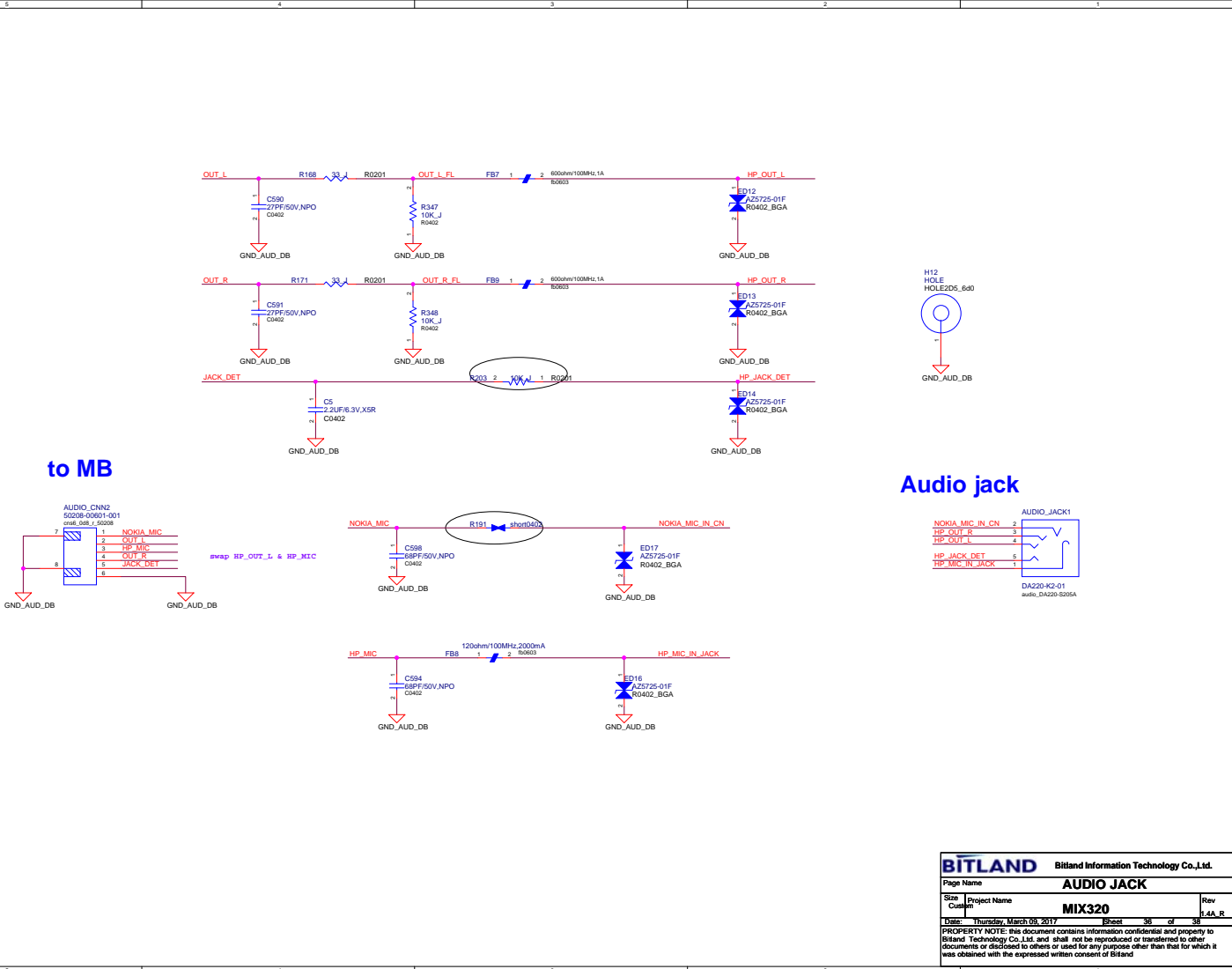


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+V3P3A_PLT 12,16,17,28
+V1P2A 11,16



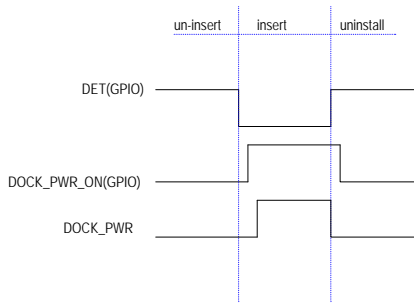
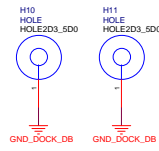
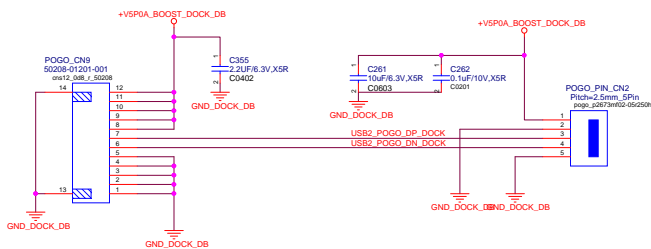






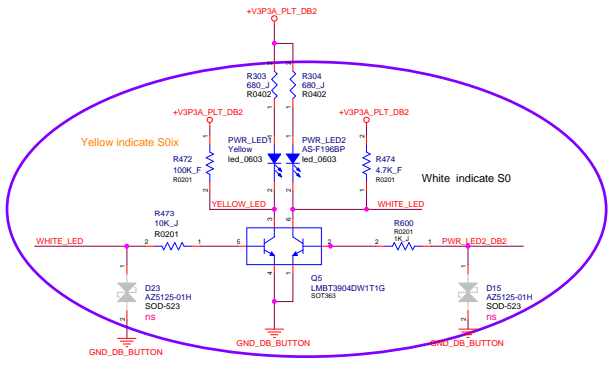
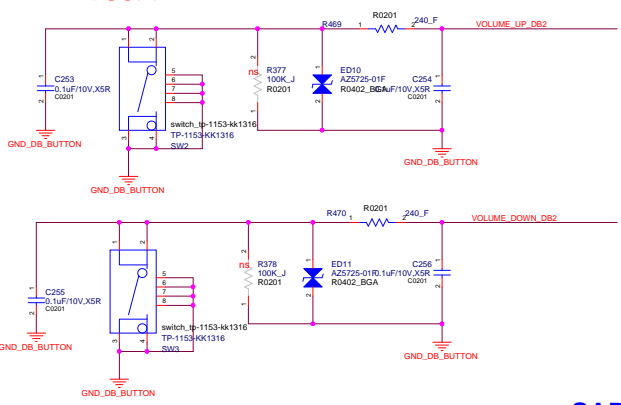
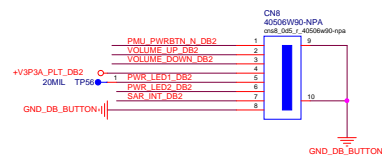
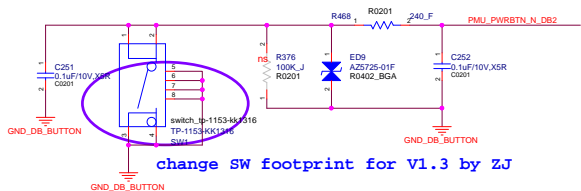
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POGO PIN connector at DOCK front side

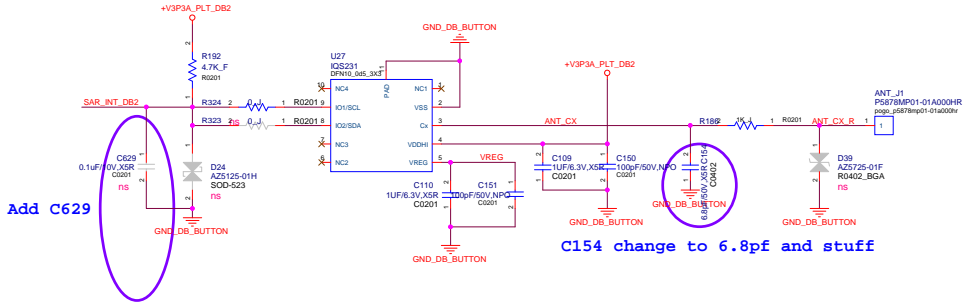


delete USB Redriver

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SAR sensor



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